

10/9/03



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT: Manjunath D. Haritsa et al.
SERIAL NO.: 09/982,452
FILING DATE: October 17, 2001
TITLE: CLOCK SKEW VERIFICATION METHODOLOGY
FOR GRID-BASED DESIGN
EXAMINER: Tat, Binh C.
ART UNIT: 2825

CERTIFICATE OF MAILING

I hereby certify that this paper is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, P. O. Box 1450, Alexandria, VA 22313-1450, on the date printed below:

Date: 9-23-03Name: Diane Morse
Diane Morse

RESPONSE TO OFFICE ACTION

COMMISSIONER FOR PATENTS
P.O. BOX 1450
ALEXANDRIA, VA 22313-1450

Dear Sir:

This paper is in response to the Office Action dated July, 14, 2003. Please consider the following.

RECEIVED
OCT - 6 2003
TECHNOLOGY CENTER 2800